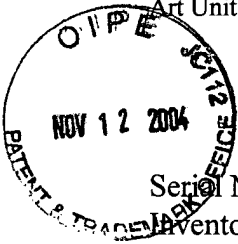


Serial Nr.: 10/024,844

2001:UPA-01228

Art Unit: 2111



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: **10/024,844**

Examiner: **Kenneth S. Kim**

Inventor: **Pao-Lung Chen and Chen-Yi Lee**

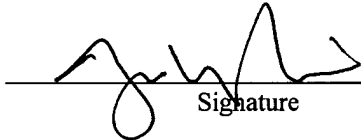
Filed: **December 18, 2001**

Art Unit: **2111**

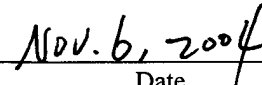
Title: **Architecture of Method For Fetching Microprocessor's Instructions**

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Formal Drawings, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.



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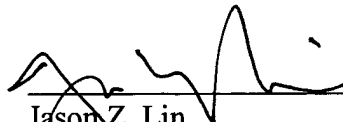
Alexandria, VA 22313-1450

Attn.: Official Draftsperson

Sir:

Enclosed are **six** sheets of the formal drawings for FIGs. **1-6** for correcting informalities and filing in the above-identified U.S. patent application.

Respectfully submitted,



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